

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	624	((pin spacer support\$4) with (board PCB PWB CB card) with (position coordinate location placement plac \$4)) and Fuji.as.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/12/04 14:39
L2	125	((pin spacer support\$4) with (board PCB PWB CB card) with (position coordinate location placement plac \$4)) and (superpos\$7 superimpos\$7 overlap\$7) and Fuji.as.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/12/04 14:39
L3	58	((pin spacer support\$4) with (board PCB PWB CB card) with (position coordinate location placement plac \$4)) and ((superpos\$7 superimpos\$7 overlap\$7) same (bottom top side)) and Fuji.as.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/12/04 14:40

L4	30	((pin spacer support\$4) with (board PCB PWB CB card) with (position coordinate location placement plac \$4)) and ((superpos\$7 superimpos\$7) same (bottom top side)) and Fuji.as.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/12/04 14:41
L5	2127	((pin spacer support\$4) with (board PCB PWB CB card) with (position coordinate location placement plac \$4)) and ((superpos\$7 superimpos\$7) same (bottom top side))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/12/04 14:49
L6	723	((pin spacer support\$4) with (board PCB PWB CB card) with (position coordinate location placement plac \$4)) and ((superpos\$7 superimpos\$7) same (bottom top side) same (imag \$4 display\$4 CAD design\$4 interface))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/12/04 14:50

L7	205	((pin spacer support\$4) with (board PCB PWB CB card) with (position coordinate location placement plac \$4)) and ((superpos\$7 superimpos\$7) same (bottom top side) same (imag \$4 display\$4 CAD design\$4 interface))	US-PGPUB; USPAT	OR	ON	2008/12/04 14:51
L8	1	((pin spacer support\$4) with (board PCB PWB CB card) with (position coordinate location placement plac \$4)) and ((superpos\$7 superimpos\$7) same (bottom top side) same (imag \$4 display\$4 CAD design\$4 interface)) and ((Teruyuki with Oohashi) (Takashi with Kurashina)). in.	US-PGPUB; USPAT	OR	ON	2008/12/04 14:54
L9	928	((pin spacer support\$4) with (board PCB PWB CB card) with (position coordinate location placement plac \$4)) and ((superpos\$7 superimpos\$7) same (bottom top side) same (imag \$4 display\$4 CAD design\$4 interface))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/12/04 14:55

		((Teruyuki T with Oohashi) ((Takashi T) with Kurashina)).in.				
L10	1286	703/1.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/12/04 14:58
L11	1	((pin spacer support\$4) with (board PCB PWB CB card) with (position coordinate location placement plac \$4)) and ((superpos\$7 superimpos\$7) same (bottom top side) same (imag \$4 display\$4 CAD design\$4 interface)) and (((Teruyuki T) with Oohashi) ((Takashi T) with Kurashina)).in.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/12/04 14:58
L12	706	700/97.ccls.	USPAT	OR	ON	2008/12/04 15:19
L13	731	703/1.ccls.	USPAT	OR	ON	2008/12/04 15:19
L14	1518	716/1.ccls.	USPAT	OR	ON	2008/12/04 15:19
L15	9524	"716"/.ccls.	USPAT	OR	ON	2008/12/04 15:19
L16	1938	716/5.ccls.	USPAT	OR	ON	2008/12/04 15:19
L17	953	716/8.ccls.	USPAT	OR	ON	2008/12/04 15:19
L18	15	(superpos\$7 and image and surface and reverse and component).clm.	USPAT	OR	ON	2008/12/04 15:20
L19	706	700/97.ccls.	USPAT	OR	ON	2008/12/04 15:20

L20	471	700/97.ccls.	US-PGPUB	OR	ON	2008/12/04 15:21
L21	552	703/1.ccls.	US-PGPUB	OR	ON	2008/12/04 15:21
L22	369	703/13.ccls.	US-PGPUB	OR	ON	2008/12/04 15:21
L23	543	716/1.ccls.	US-PGPUB	OR	ON	2008/12/04 15:21
L24	887	716/5.ccls.	US-PGPUB	OR	ON	2008/12/04 15:21
L25	362	716/8.ccls.	US-PGPUB	OR	ON	2008/12/04 15:21
L26	19	(superpos\$7 and image and surface and reverse and component).clm.	US-PGPUB; USPAT	OR	ON	2008/12/04 15:22
L27	4	(superpos\$7 and image and surface and reverse and component).clm.	US-PGPUB	OR	ON	2008/12/04 15:22
L28	953	361/749.ccls.	US-PGPUB; USPAT	OR	ON	2008/12/04 15:23
L29	1	(backup and device and support and place and position and board and display and mounting). clm.	US-PGPUB	OR	ON	2008/12/04 15:25
S1	2	"6275374".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/24 12:10
S2	4	((Teruyuki with Oohashi) (Takashi with Kurashina)). in.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/24 12:39

S3	3666	(700/95 700/97 700/114 700/182). ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/24 12:42
S4	15494	("716"/\$).ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/24 12:42
S5	5113	(703/13 703/14 703/15 703/16 703/17 703/18 703/19 703/20 703/21 703/22). ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/24 12:43
S6	1607	(361/748 361/758 361/770).ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/24 12:44
S7	3740	(29/739 29/740). ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/24 12:44
S8	8135	(prevent\$7 with (bend\$4 flex\$7) with (board PCB PWB CB card))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/24 12:52
S9	10387	(prevent\$7 with (bend\$4 flex\$7 warp\$4) with (board PCB PWB CB card))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/24 12:57

S10	513	(prevent\$7 with (bend\$4 flex\$7 warp\$4) with (board PCB PWB CB card)) and (input\$4 with (position coordinate spacer pin locat\$4 plac\$5 set\$4 support\$4))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/24 13:03
S11	513	(prevent\$7 with (bend\$4 flex\$7 warp\$4) with (board PCB PWB CB card)) and (input\$4 with (position coordinate spacer pin locat\$4 plac\$5 set\$4 support\$4))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/24 13:09
S12	124	S10 and ((pin spacer support\$4) with (board PCB PWB CB card) with (position coordinate location placement plac \$4))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/24 13:13
S13	113	S10 and ((pin spacer support\$4) with (board PCB PWB CB card) with (position coordinate location placement plac \$4)) and mount\$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/24 13:19
S14	113	S12 and mount\$4	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/24 13:19
S15	6	S9 and "716"/\$. ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/09/24 13:45

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